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To: Examiner Hsien-Ming Lee  
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From: Timothy M. Honeycutt  
Client/Matter: Serial No.: 10/085,903 (Atty. Dkt. No. AMDI:115\HON)  
Date: October 9, 2003

DOCUMENTS	NUMBER OF PAGES*
Response to the Office Action Mailed June 4, 2003	11

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Andy Wei et al.

§ Group No.: 2823

Serial No.: 10/085,903

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Filed: February 28, 2002

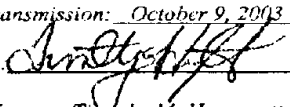
For: SOI MOSFET JUNCTION  
DEGRADATION USING MULTIPLE  
BURIED AMORPHOUS LAYERS

Examiner: Hsien Ming Lee

Atty. Dkt. No.: AMDI:115\HON

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

**RESPONSE TO THE OFFICE ACTION MAILED SEPTEMBER 25, 2003**

This paper is responsive to the Office Action Mailed September 25, 2003. If the Examiner believes that a telephone conference will advance the prosecution of the above-captioned application, the Examiner is invited to contact the undersigned at (281) 379-7999. Please amend the above-identified application as follows.

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